

Appl. No. 10/707,416
Amdt. dated August 18, 2005
Reply to Office action of May 18, 2005

Amendments to the Claims:

Listing of Claims:

Claim 1 (currently amended) A one drop fill (ODF) liquid crystal display (LCD) panel comprising:

- 5 a first substrate comprising:
- a pixel array region positioned on a central part of the first substrate, the pixel array region comprising a plurality of pixels, and each pixel including a thin film transistor (TFT);
- 10 a sealant region positioned on a bordering part of the first substrate and surrounding the pixel array region; and
- a light-shielding pattern positioned on a surface of the sealant region, the light-shielding pattern comprising a first metal pattern, a second metal pattern, and a first insulating layer positioned between the first metal pattern and the second metal pattern;
- 15 a second substrate;
- a sealant pattern positioned between the first substrate and the second substrate and corresponding to the sealant region for combining the first substrate with the second substrate; and
- 20 a liquid crystal layer positioned between the first substrate and the second substrate;
- wherein when the first metal pattern is connected to a gate of the TFTs, the second metal pattern is used to prevent from
- 25 light leakage through the first metal pattern, and when the second metal pattern is connected to a source or a drain of the TFTs, the first metal pattern is used to prevent from light

Appl. No. 10/707,416
Amdt. dated August 18, 2005
Reply to Office action of May 18, 2005

leakage through the second metal pattern.

Claim 2 (original) The ODF LCD panel of claim 1 wherein the sealant
pattern comprises a photocuring sealant.

5

Claim 3 (cancelled)

Claim 4 (cancelled)

10 Claim 5 (cancelled)

Claim 6 (cancelled)

15 Claim 7 (currently amended) The ODF LCD panel of claim [[3]] 1 wherein
the first metal pattern and the second metal pattern further
comprise at least a second insulating layer for preventing a
coupling effect from occurring between the first metal pattern
and the second metal pattern.

20 Claim 8 (cancelled)

Claim 9 (original) The ODF LCD panel of claim 1 wherein the sealant
pattern is positioned in the sealant region of the first substrate.

25 Claim 10 (original) The ODF LCD panel of claim 1 wherein the sealant
pattern is positioned on a surface of the second substrate
corresponding to the sealant region of the first substrate.

Appl. No. 10/707,416
Amdt. dated August 18, 2005
Reply to Office action of May 18, 2005

Claim 11 (currently amended) A display panel comprising:

a first substrate comprising:

a pixel array region positioned on a central part of the first substrate, the pixel array region comprising a plurality of pixels, and each pixel including a thin film transistor (TFT);

a sealant region positioned on a bordering part of the first substrate and surrounding the pixel array region; and

a light-shielding pattern positioned on a surface of the sealant region, the light-shielding pattern further comprising a first metal pattern and a second metal pattern;

a second substrate; and

a sealant pattern positioned between the first substrate and the second substrate and corresponding to the sealant region for combining the first substrate with the second substrate; wherein when the first metal pattern is connected to a gate of the TFTs, the second metal pattern is used to prevent from light leakage through the first metal pattern, and when the second metal pattern is connected to a source or a drain of the TFTs, the first metal pattern is used to prevent from light leakage through the second metal pattern.

Claim 12 (original) The display panel of claim 11 wherein the sealant pattern comprises a photocuring sealant.

Claim 13 (cancelled)

Appl. No. 10/707,416
Amdt. dated August 18, 2005
Reply to Office action of May 18, 2005

Claim 14 (cancelled)

Claim 15 (cancelled)

5 Claim 16 (cancelled)

10 Claim 17 (currently amended) The display panel of claim ~~[[13]]~~ 11 wherein
the first metal pattern and the second metal pattern further
comprise at least an insulating layer for preventing a coupling
effect from occurring between the first metal pattern and the
second metal pattern.

15 Claim 18 (original) The display panel of claim 11 wherein the display panel
is a one drop fill (ODF) LCD panel.

Claim 19 (cancelled)

20 Claim 20 (original) The display panel of claim 11 wherein the sealant
pattern is positioned in the sealant region of the first substrate.

Claim 21 (original) The display panel of claim 11 wherein the sealant
pattern is positioned on a surface of the second substrate
corresponding to the sealant region of the first substrate.